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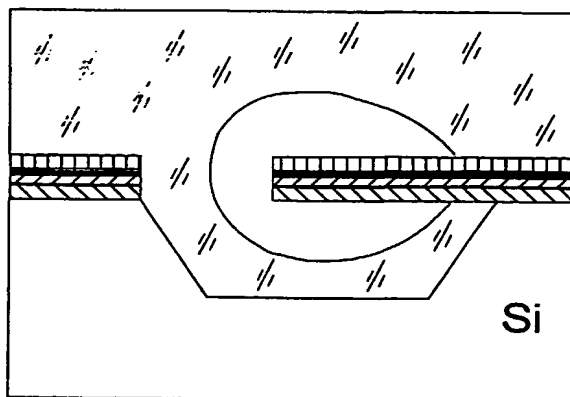
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(54) Title: METHOD FOR PRODUCING A PACKAGED INTEGRATED CIRCUIT



(57) Abstract: There is described a method for producing a packaged integrated circuit. The method comprises a first step of building an integrated circuit having a micro-structure suspended above a micro-cavity, and having a heating element on the micro-structure capable of heating itself and its immediate surroundings. A layer of protective material is then deposited on said micro-structure such that at least a top surface of the micro-structure and an opening of the micro-cavity is covered, wherein the protective material is in a solid state at room temperature and can protect the micro-structure during silicon wafer dicing procedures and subsequent packaging. The integrated circuit is packaged and an electric current is passed through the heating element such that a portion of the protective material is removed and an unobstructed volume is provided above and below the micro-structure.

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## INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 B81B7/00 B81B3/00

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 B81B B81C G01K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC, COMPENDEX

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6 379 988 B1 (CONLEY WILLIAM R ET AL) 30 April 2002 (2002-04-30) figures 4-7 column 10, line 49 - column 14, line 3	1-43
A	WO 02/18785 A (CANTOR HAL C ; HOWER ROBERT W (US); MONDRO JASON R (US); ADVANCED SENS) 7 March 2002 (2002-03-07) figure 3 page 3, line 1 - page 10, line 27 ----- -/--	1-43

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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C.(Continuation) DOCUMENT CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	ELDERSTIG H ET AL: "Spin deposition of polymers over holes and cavities" SENSORS AND ACTUATORS A, ELSEVIER SEQUOIA S.A., LAUSANNE, CH, vol. 46, no. 1-3, January 1995 (1995-01), pages 95-97, XP004303460 ISSN: 0924-4247 figures 1-3 paragraph '0002! -----	1-43
A	US 2001/055864 A1 (SHI LEATHEN ET AL) 27 December 2001 (2001-12-27) figures 4-10 paragraphs '0037! - '0048! -----	1-43
A	GRUDIN O ET AL: "Microstructure release and test techniques for high-temperature micro hotplate" ELECTRICAL AND COMPUTER ENGINEERING, 1999 IEEE CANADIAN CONFERENCE ON EDMONTON, ALTA., CANADA 9-12 MAY 1999, PISCATAWAY, NJ, USA, IEEE, US, 9 May 1999 (1999-05-09), pages 1610-1615, XP010359723 ISBN: 0-7803-5579-2 figures 1,2 paragraphs 'EXPERIMENTS!, 'OAND!, 'OBSERVATIONS! -----	1-43

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Information on patent family members

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